

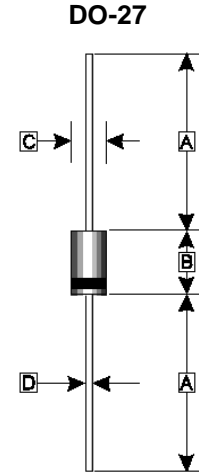
RoHS Compliant Product  
A suffix of "-C" specifies halogen & lead-free

### FEATURES

- Fast switching for high efficiency
- Low forward voltage drop
- High current capability
- Low reverse leakage current
- High surge current capability

### PACKAGING INFORMATION

- Case: Molded plastic DO-27
- Epoxy: UL 94V-0 rate flame retardant
- Terminals: Solderable per MIL-STD-202 method 208
- Polarity: Color band denotes cathode
- Mounting position: Any
- Weight: 1.1 grams (approximately)



REF.	Millimeter	
	Min.	Max.
A	25.4 (TYP)	
B	7.20	9.50
C	4.80	5.60
D	1.10	1.30

### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating 25°C ambient temperature unless otherwise specified. Single phase half wave, 60Hz, resistive or inductive load.  
For capacitive load, de-rate current by 20%.)

Parameter	Symbol	Rating	Unit	
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	800	V	
Working Peak Reverse Voltage	$V_{RWM}$	560	V	
Maximum DC Blocking Voltage	$V_{DC}$	800	V	
Maximum Average Forward Rectified Current @ $T_A=75^\circ\text{C}$	$I_{F(AV)}$	4	A	
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	$I_{FSM}$	125	A	
Maximum Instantaneous Forward Voltage	$V_F$	$I_F=4\text{A}$	1.85	V
		$I_F=3\text{A}$	1.8	
Maximum DC Reverse Current at Rated DC Blocking Voltage	$I_R$	$T_J=25^\circ\text{C}$	25	$\mu\text{A}$
		$T_J=150^\circ\text{C}$	250	
Maximum Reverse Recovery Time @ $I_F=0.5\text{A}$ , $I_R=1\text{A}$ , $I_{rr}=0.25\text{A}$	$T_{RR}$	75	nS	
Operating Temperature Range	$T_J$	-65 ~ 175	$^\circ\text{C}$	
Storage Temperature Range	$T_{STG}$	-65 ~ 175	$^\circ\text{C}$	

Notes:

1. Pulse Test:  $t_p=300\mu\text{s}$ , Duty Cycle < 2%
2. Lead length=1/2" on P.C. board with 1.5"x1.5" copper surface

**RATINGS AND CHARACTERISTIC CURVES**

FIG.1 FORWARD CURRENT DERATING CURVE

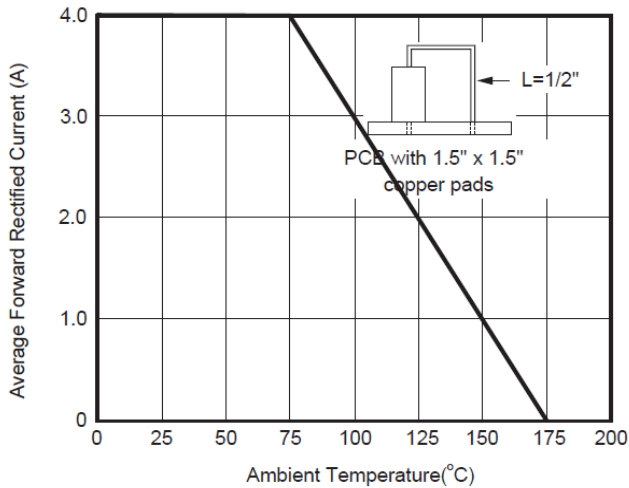


FIG.2 MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

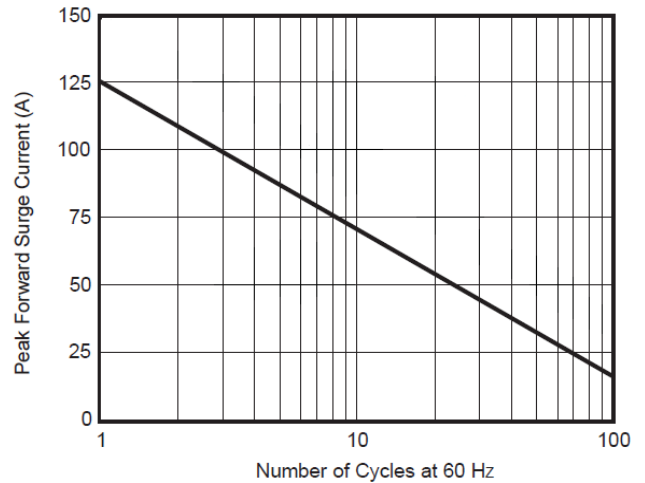


FIG. 3 TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

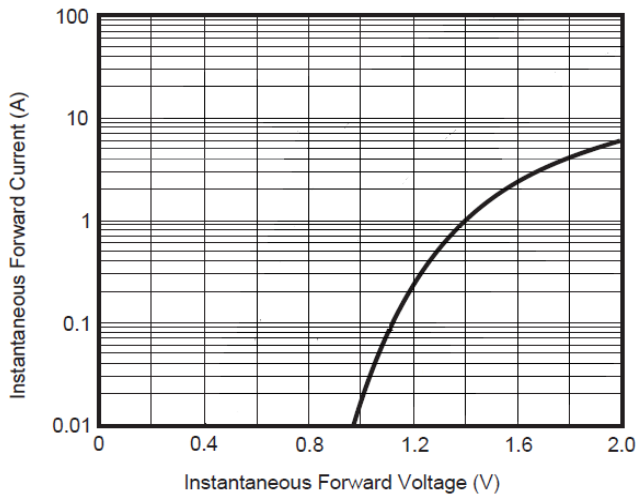


FIG. 4. TYPICAL REVERSE CHARACTERISTICS

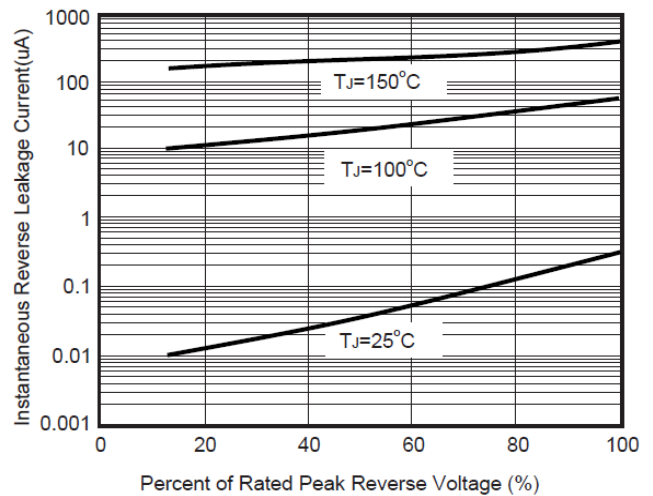


FIG.5 TYPICAL JUNCTION CAPACITANCE PER LEG

